

PhD Internship in IBM, Bangalore, India



Mr. Dheeraj Sharma, PhD Scholar, Nanoscale Devices, VLSI/ULSI Circuit and System Design R & D Lab, Discipline of Electrical Engineering, School of Engineering, IIT Indore has been offered a PhD Internship for six months (1st Aug. 2013 - 31st Jan. 2014) in IBM Bangalore, India.

During his internship, he will work with Dr. Devesh Dwivedi, Manager, SRAM Development, Systems and Technology Group, IBM, Banagalore on the topic “FinFET based circuit optimization for SRAMs”.

He is currently pursuing his research in the area of “Unified and Compact Multigate Gate-All-Around MOSFETs Modeling and its Circuit Implementation” under the supervision of Dr. Santosh Kumar Vishvakarma, Assistant Professor, Electrical Engineering, IIT Indore.